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7 Compiler Optimization of Memory-Resident Value Communication Between Speculative Threads
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March 2004 **Proceedings of the international symposium on Code generation and optimization: feedback-directed and runtime optimization**

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8 Value prediction in VLIW machines
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10 Value speculation scheduling for high performance processors
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11 Accurate static branch prediction by value range propagation
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Antonia Zhai, Christopher B. Colohan, J. Gregory Steffan, Todd C. Mowry
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14 Selective value prediction
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18 Precise compile-time performance prediction for superscalar-based computers
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19 Technical correspondence: Value reuse optimization: reuse of evaluated math library function calls through compiler generated cache
K. V. Seshu Kumar
August 2003 **ACM SIGPLAN Notices**, Volume 38 Issue 8
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20 Improving branch predictors by correlating on data values
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